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Terms	Documents
L6 and (subsequent or subsequently)	527

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<u>L7</u>	L6 and (subsequent or subsequently)	527	<u>L7</u>
<u>L6</u>	L3 and L5	661	<u>L6</u>
<u>L5</u>	"improve performance" or "improved performance" or "improving performance"	138651	<u>L5</u>
<u>L4</u>	-"improved performance" or "improving performance"	74703	<u>L4</u>
<u>L3</u>	L1 and L2	3386	<u>L3</u>
<u>L2</u>	iteration or iteratively	68358	<u>L2</u>
<u>L1</u>	"parallel processor" or "parallel processing"	30655	<u>L1</u>

### **END OF SEARCH HISTORY**

### **Refine Search**

#### Search Results -

Terms	Documents
L8 and averag\$3	305

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<u>L12</u>	L8 and averag\$3	305	<u>L12</u>
<u>L11</u>	L8 and L10	0	<u>L11</u>
<u>L10</u>	averag\$3 near histor\$4	770	<u>L10</u>
<u>L9</u>	L8 and (averag\$3 near (histor\$4))	0	<u>L9</u>
<u>L8</u>	L7 and error	424	<u>L8</u>
<u>L7</u>	L6 and (subsequent or subsequently)	527	<u>L7</u>
<u>L6</u>	L3 and L5	661	<u>L6</u>
<u>L5</u>	"improve performance" or "improved performance" or "improving performance"	138651	<u>L5</u>
<u>L4</u>	"improved performance" or "improving performance"	74703	<u>L4</u>
<u>L3</u>	L1 and L2	3386	<u>L3</u>
<u>L2</u>	iteration or iteratively	68358	<u>L2</u>
<u>L1</u>	"parallel processor" or "parallel processing"	30655	<u>L1</u>

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## Refine Search

#### Search Results -

Terms	Documents
L15 and ensembles	2694

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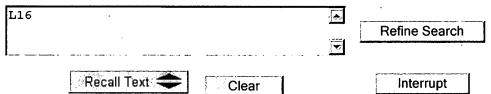
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DB=	PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
<u>L16</u>	L15 and ensembles	2694	<u>L16</u>
<u>L15</u>	L13 or L14	197381	<u>L15</u>
<u>L14</u>	"boltzmann machine"	178	<u>L14</u>
<u>L13</u>	neur\$8 or ("threshold logic unit" or tlu) or ("threshold logic gate" or TLG) or (adaline or madaline or perceptron) or (self?organizing adj map) or (self?organizing adj net) or (self?organizing adj network) or (self?organizing adj feature)	197362	<u>L13</u>
<u>L12</u>	L8 and averag\$3	305	<u>L12</u>
<u>L11</u>	L8 and L10	0	<u>L11</u>
<u>L10</u>	averag\$3 near histor\$4	770	<u>L10</u>
<u>L9</u>	L8 and (averag\$3 near (histor\$4))	. 0	<u>L9</u>
<u>L8</u>	L7 and error	424	<u>L8</u>
<u>L7</u>	L6 and (subsequent or subsequently)	527	<u>L7</u>

<u>L6</u>	L3 and L5	661	<u>L6</u>
<u>L5</u>	"improve performance" or "improved performance" or "improving performance"	138651	<u>L5</u>
<u>L4</u>	"improved performance" or "improving performance"	74703	<u>L4</u>
<u>L3</u>	L1 and L2	3386	<u>L3</u>
<u>L2</u>	iteration or iteratively	68358	<u>L2</u>
<u>L1</u>	"parallel processor" or "parallel processing"	30655	<u>L1</u>

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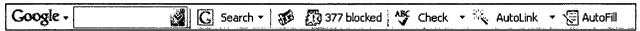
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